IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: DeYoung et al.

Application Serial No.: Not Yet Assigned Group Art Unit: Not Yet Assigned

Filed: Concurrently Herewith

For: Method of Treating Microelectronic Substrates

Date: January 22, 2004

MAIL STOP PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Sir:

Attached is a list of documents on Form PTO-1449, together with a copy of any listed foreign patent document and/or non-patent literature. A copy of any listed U.S. patent and/or U.S. patent application publication is not provided herewith in accordance with the waiver by the U.S. Patent and Trademark Office of requirements under 37 C.F.R. § 1.98(a)(2)(i) for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003.

It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.56 and Section 609 of the MPEP. No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted

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| Substitute form 1449A/PTO | | | | C | Complete if Known | | |
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| 9 | | | | Application Number | Not Yet Assigned | | |
| INFORMATION DISCLOSURE | | | | Filing Date | Concurrently Herewith | | |
| STATEM | ENT BY APPL | _ICAN | Т | First Named Inventor | DeYoung et al. | | |
| 0 17 11 211 | | | • | Group Art Unit | Not Yet Assigned | | |
| (use as many sheets as necessary) | | | | Examiner Name | Not Yet Assigned | | |
| Sheet | 1 | of | 1 | Attorney Docket Number | 5697-64 | | |

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| Examiner Signature | Date Considered | |
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